

**Notice of References Cited**

Application/Control No.

09/276,803

Applicant(s)/Patent Under

Reexamination

LEE ET AL.

Examiner

Neal Berezny

Art Unit

2823

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5439845	08-1995	Watanabe et al.	437/130
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	Q					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., Tauber, R. N.; Silicon Processing for the VLSI Era; Lattice Press, Sunset Beach, Ca.; 1986; pp.57,183, 388-389.
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	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.